CLAIMS

1. (previously amended) A chip rate base band processor which receives digital information containing symbol information and provides a symbol output, comprising:

an input memory which stores the digital information;

a data PN code buffer;

a pilot PN code buffer;

a pilot multiplier having a first input coupled to the pilot PN code buffer, a second input coupled to the input memory, and an output;

a data multiplier having a first input coupled to the data PN code buffer, a second input coupled to the input memory, and an output;

a pilot correlator having an input coupled to the output of the first multiplier, and an output;

a pilot memory coupled to the pilot correlator;

a channel estimator coupled to the pilot memory;

a peak detector coupled to the pilot memory;

a data correlator coupled to the data multiplier;

load controller having a first input coupled to the peak detector, a second input coupled to data correlator, and an output;

a data memory coupled to the load controller;

a phase rotator having a first input coupled to the channel estimator, a second input coupled to the data memory, and an output; and

a symbol combiner having an input coupled to the phase rotator, and an output which provides the symbol output.

2. (original) The chip rate base band processor of claim 1 further comprising a cluster tracker having an input coupled to the pilot memory, and an output coupled to the pilot PN code buffer.

3. (original) The chip rate base band processor of claim 1 wherein the output of the cluster tracker is coupled to the data PN code buffer.

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4-6. (cancelled).

7. (currently amended) In a chip rate base band receiver processor which receives digital information containing symbol information, wherein each symbol of the symbol information is of a predetermined time duration, a method comprising the steps of:

storing <u>a first scan window of</u> the digital information;

<u>scanning the first scan window for all instances of a first symbol of the symbol information;</u>

~~multiplying a PN code with a first segment, corresponding to a first multi-path and representative of the predetermined time duration, of the stored digital information and~~

<u>storing a second scan window of the digital information; and</u>

<u>scanning the second scan window for all instances of a second symbol of the symbol information</u>

~~multiplying the PN code with a second segment, corresponding to a second multi-path and representative of the predetermined time duration, of the stored digital information.~~

8. (cancelled)

9. (currently amended) The method of claim <u>7</u> ~~8~~, wherein scanning the first scan window is achieved by a first PN code and scanning the second scan window is achieved with a second PN code.

10-17. (cancelled)

18. (new claim) The method of claim 7, wherein the second symbol immediately follows the first symbol in the symbol information.

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19. (new claim) The method of claim 7, wherein the second scan window overlaps the first scan window.